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Made in IBM Labs : Holey Optochip transfère un téraoctet d'information par seconde en utilisant la puissance de la lumière

Researchers invent novel technique to boost data transfer rates of existing parallel optical communications technology

Until now, it was not possible to transport terabits of data from a single quarter-inch chip, smaller than the size of a dime

New prototype compactly and efficiently delivers ultra-high interconnect bandwidth to power future supercomputer and data center applications

Paris - 09 mars 2012: IBM (NYSE: [IBM](#)) / (#ibmresearch) scientists today will report on a prototype optical chipset, dubbed holey Optochip, that is the first parallel optical transceiver to transfer one trillion bits – one terabit – of information per second, the equivalent of downloading 500 high definition movies. The report will occur at the [Optical Fiber Communication Conference](#) taking place in Los Angeles, Calif.

With the ability to move information at blazing speeds – 8 times faster than parallel optical components available today – the breakthrough could transform how data is accessed, shared and used for a new era of communications, computing and entertainment. The raw speed of one transceiver is equivalent to the bandwidth consumed by 100,000 users at today's typical 10 Mb/s high-speed internet access. Or, it would take just around an hour to transfer the entire U.S. Library of Congress web archive through the transceiver.

Progress in optical communications is being driven by an explosion of new applications and services as the amount of data being created and transmitted over corporate and consumer networks continues to grow. At one terabit per second, IBM's latest advance in optical chip technology provides unprecedented amounts of bandwidth that could one day ship loads of data such as posts to social media sites, digital pictures and videos posted online, sensors used to gather climate information, and transaction records of online purchases.

*"Reaching the one trillion bit per second mark with the holey Optochip marks IBM's latest milestone to develop chip-scale transceivers that can handle the volume of traffic in the era of big data," said **IBM Researcher Clint Schow**, part of the team that built the prototype. "We have been actively pursuing higher levels of integration, power efficiency and performance for all the optical components through packaging and circuit innovations, and we aim to improve on the technology for commercialization in the next decade with the collaboration of our manufacturing partners."*

Optical networking offers the potential to significantly improve data transfer rates by speeding the flow of data using light pulses, instead of sending electrons over wires. Because of this, researchers have been looking for ways to make use of optical signals within standard low-cost, high-volume chip manufacturing techniques for widespread use.

Using a novel approach, scientists in the IBM labs have developed a holey Optochip by fabricating 48 holes

through a standard silicon CMOS chip. The holes allow optical access through the back of the chip to 24 receiver and 24 transmitter channels to produce an ultra-compact, high-performing and power-efficient optical module capable of record setting data transfer rates.

The compactness and capacity of optical communication has become indispensable in the design of large data-handling systems. With that in mind, the holey Optochip module is constructed with components that are commercially available today providing the possibility to manufacture at economies of scale.

Consistent with green computing initiatives, the holey Optochip achieves record speed at a power efficiency (the amount of power required to transmit a bit of information) that is among the best ever reported

. The transceiver consumes less than 5 watts; the power consumed by a 100W light bulb could power 20 transceivers. This progress in power efficient interconnects is necessary to allow companies who adopt high-performance computing to manage their energy load while performing powerful applications such as analytics, data modeling and forecasting.

By demonstrating unparalleled levels of performance, the holey Optochip illustrates that high-speed, low-power interconnects are feasible in the near term and optical is the only transmission medium that can stay ahead of the accelerating global demand for broadband. The future of computing will rely heavily on optical chip technology to facilitate the growth of big data and cloud computing and the drive for next-generation data center applications.

Technical Aspects of the Holey Optochip

Parallel optics is a fiber optic technology primarily targeted for high-data, short-reach multimode fiber systems that are typically less than 150 meters. Parallel optics differs from traditional duplex fiber optic serial communication in that data is simultaneously transmitted and received over multiple optical fibers.

A single 90-nanometer IBM CMOS transceiver IC with 24 receiver and 24 transmitter circuits becomes a holey Optochip with the fabrication of forty-eight through-silicon holes, or “optical vias”: one for each transmitter and receiver channel. Simple post-processing on completed CMOS wafers with all devices and standard wiring levels results in an entire wafer populated with holey Optochips. The transceiver chip measures only 5.2 mm x 5.8 mm, about one-eighth the area of a dime. Twenty-four channel, industry-standard 850-nm VCSEL (vertical cavity surface emitting laser) and photodiode arrays are directly flip-chip soldered to the Optochip. This direct packaging produces high-performance, chip-scale optical engines. The holey Optochips are designed for direct coupling to a standard 48-channel multimode fiber array through an efficient microlens optical system that can be assembled with conventional high-volume packaging tools.

Other Highlights at the OFC Conference

Also at the OFC Conference, IBM researchers are presenting the following advances:

- Two optical links that are the most power efficient ever reported. Underpinned by a novel receiver design, a complete single-channel VCSEL based link achieved 15Gb/s operation while consuming only 20 milliwatts of power. This represents the first practical demonstration of an optical interconnect that attains the efficiency levels that will be required for exascale computers circa 2020.
- A complete single-channel 40 Gb/s VCSEL-based optical link that not only sets a new benchmark for speed, but also operates at this high data rate with significant margin. Transmitter pre-distortion for end-to-end link performance improvement, an equalization technique that IBM has pioneered, enabled this breakthrough.

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